HiWire[™] SWITCH AEC and SONiC[®] Dual TOR Webinar

Presented by Credo with Microsoft









Hosted by 650 GROUP

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Today's Host

HiWire Switch AEC and SONiC Dual TOR Webinar



Alan Weckel | 650 Group

is a Technology Analyst/Co-Founder at 650 Group, where he is in charge of Networking and Cloud research. Alan's expertise has been quoted in CIO Today, Wall Street Journal, and Fierce Telecom. He has presented at a wide variety of industry and finance events. His work at previous companies including Cisco Systems and Raytheon provides a foundation for his deep knowledge of the industry and its supply chain.







650 Group Overview

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What We Track



Cloud

- IaaS, SaaS, Colo, Search and Social
- CAPEX, Equipment Trends



Telecom Equipment

- Broadband Access, Telecom Core
- NFV, Mobile RAN, SP Routing, Optical Transport

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Data Center Equipment

- Data Center Switching, Servers, Storage
- Merchant Silicon, DCI, Security



Enterprise Networking Equipment

- Switching, WLAN, Security
- Enhanced NAC, Unified Access, SD-WAN



Semiconductors and FABs

- Campus, DC, Cloud, IoT Semis
- ASIC trends in Switching, WLAN



Who Uses Our Research





Semiconductor Suppliers

Component Manufacturers





FABs





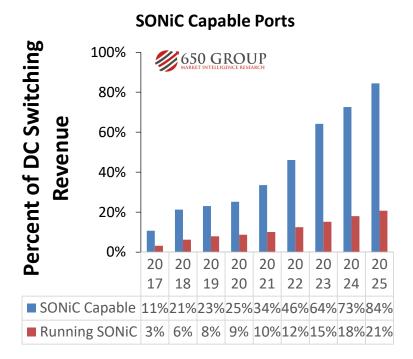
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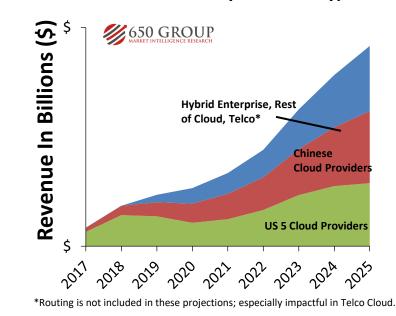
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Ethernet Switch – Data Center: SONiC Projections

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HiWire[®]

SONiC Revenue by Customer Type

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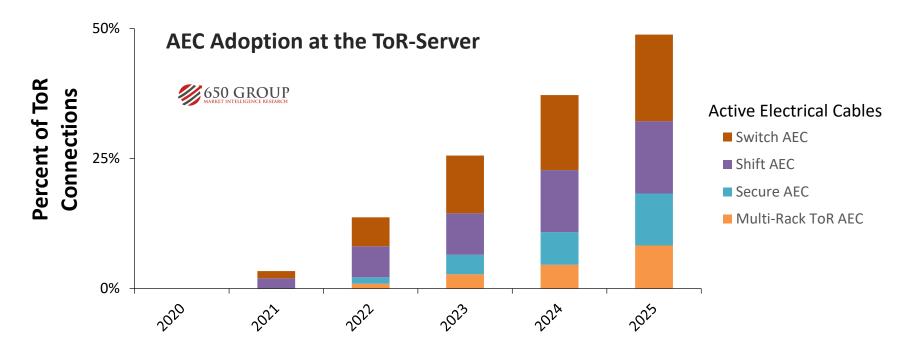
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Ethernet Switch – Data Center

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HiWire* Active Electrical Cable

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Today's Speakers

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Dr. Lihua Yuan | Microsoft

is currently a Partner Software Engineering Manager in Microsoft Azure Networking. He leads the network platform team supporting the data center network connecting major businesses including Azure, Microsoft 365, and Bing.



Don Barnetson | Credo

is the VP of Product for HiWire AECs at Credo with a focus on product definition and customer engagement for this new, exciting category.

Please ask questions in the Question Box on the right Q&A at the end of the Presentation

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Dual ToR Technology and Challenges

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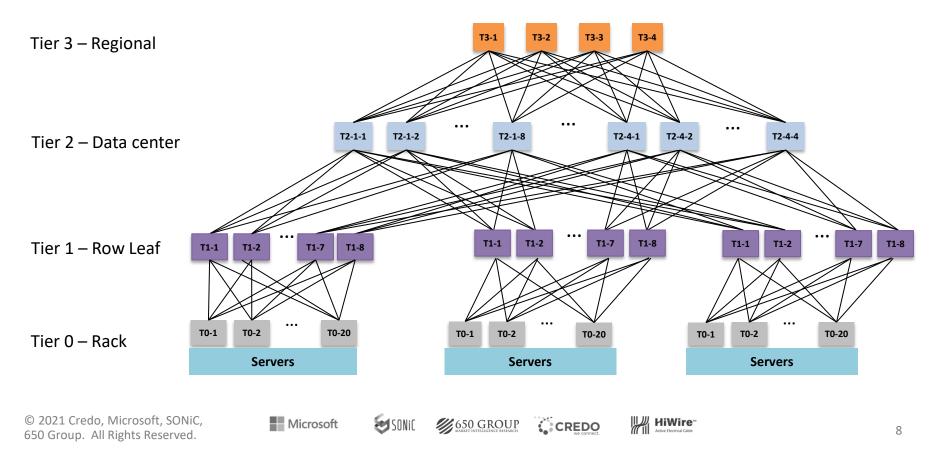






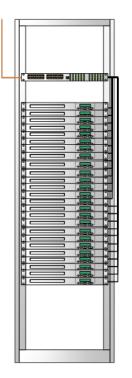
Data Center Network with CLOS

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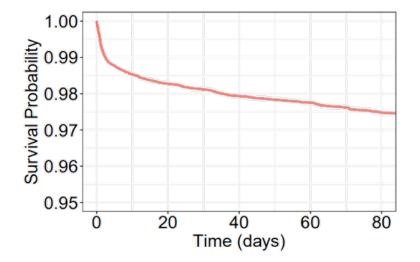


Top of Rack Switch : Single Point of Failure for a Rack

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- ToR is a Single-Point-of-Failure (SPOF) for full rack of servers
- And TORs do fail
 - ~2% of switches fail in first 3 months
 - 32% due to hardware failures
 - 27% due to power failures

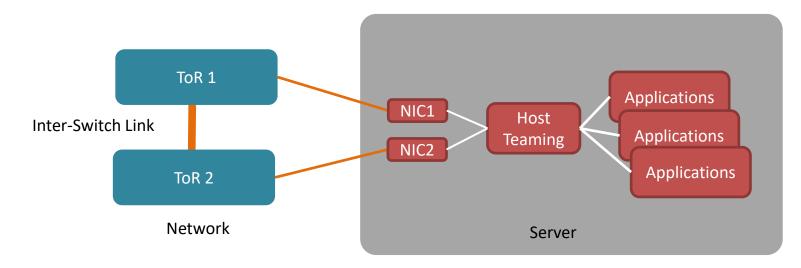






Classic Solution: Multi-Chassis LAG with Dual Uplink

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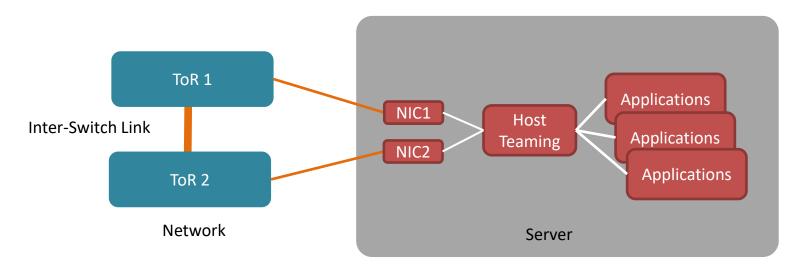






Network: Inter-Switch Link

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• Inter-Switch Link requires custom design for capacity planning

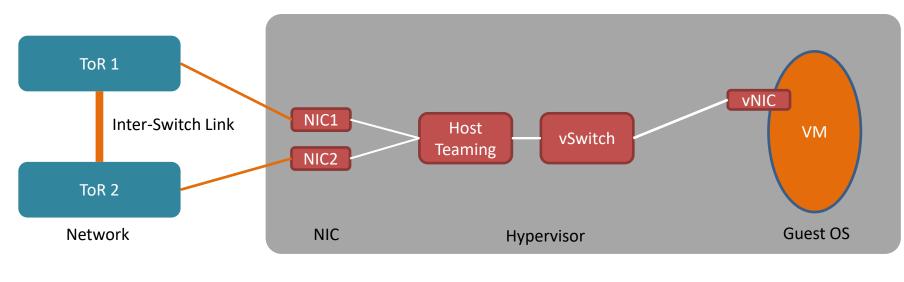
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- Requires complex state sync between ToRs
 - Creates split-brain problem when ISL fails



HyperVisor: Performance Limit

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- A few Gbps/core
- Unstable latency
- Won't work for RDMA

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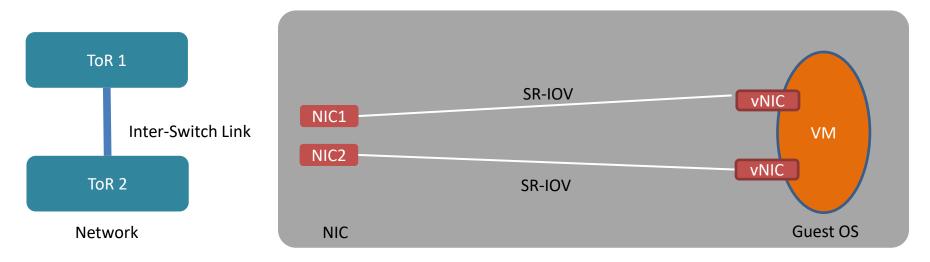
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SR-IOV: VM Complexity

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- VM sees failures and must handle
- Hardware-dependent vNIC driver

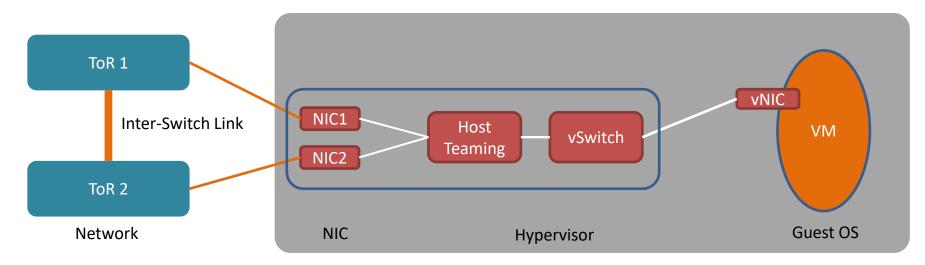
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vSwitch Offloading: Vendor Specific

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- Variety of solutions to offload Teaming and vSwitch to Smart NICs
- Capabilities and Implementations vary by NIC Vendor
- Require NIC vendor drivers in VM -> HW Dependency

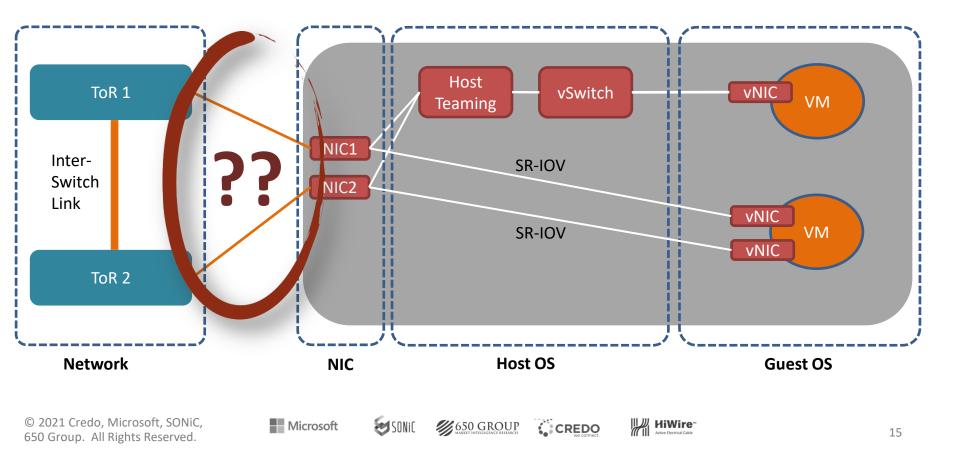


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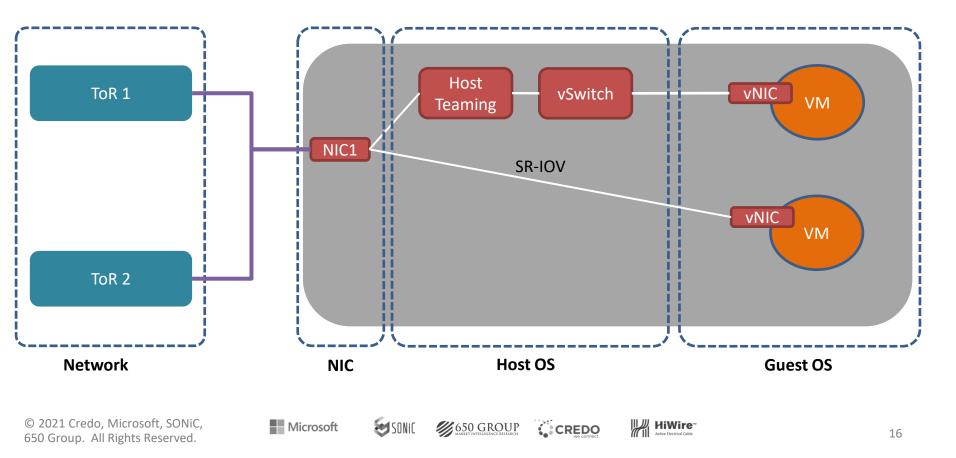
The Solution Space - Options to Manage Redundancy

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The Solution Space – Introducing the Switch AEC

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A New Approach: HiWire[™] SWITCH AEC + **SONIC Dual TOR Management Container**









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What are HiWire[™] Active Electrical Cables (AECs)

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AECs are industry-backed solutions to solve the connectivity bottleneck

- Integrate active components : retimer, gearbox, L2 Switch into Copper Cables to facilitate secure, high-integrity plug and play connectivity and mode/speed conversion.
- Compared to DACs
 - Up to 75% less weight and Volume
 - Tighter bend radius
 - Longer reach
- Compared to AOCs
 - Up to 50% less power
 - Up to 50% lower cost
 - 2.5x longer life

LP SWITCH/SWITCH AEC

Designed to enable Network Managed NIC to dual TOR connectivity in an Active/Standby configuration.



LP SHIFT / SHIFT AEC

Deliver plug and play connectivity between PAM4 ports and NRZ NICs with speed shifting and FEC termination in-cable.

LP CLOS AEC

Designed to replace backplanes in chassis with front panel interconnect used in Distributed, Disaggregated Chassis (DDC) applications.



LP SPAN/SPAN AEC

Replace AOCs with low power plug and play AECs for port-to-port and rack-to-rack connectivity.



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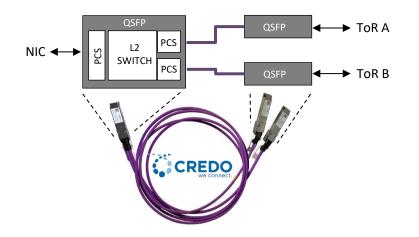
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New Solution in Collaboration with Microsoft / Azure

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Dual TOR Management Container

HiWire SWITCH AEC: 50G/100G/200G QSFP AEC Cable

- Active/Standby Layer 2 switch switching in <1 μs
- Common control plane on all 3 ends
- Fully ToR managed, works in standard QSFP NICs

Dual TOR Management Container on SONiC

- Manages the SWITCH AEC
- Manages convergence in failover conditions using standards based ARP/BGP/Encap & forward
- Free and Open Source

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HiWire SWITCH AEC

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Performance

- 50G 200G NRZ or PAM4
- Automatic switching on loss-of-signal and Manually switching on I²C command

Updates

Switching

Functionality

AEC Classification

Deployment

Roadmap

• Hitless Firmware Updates

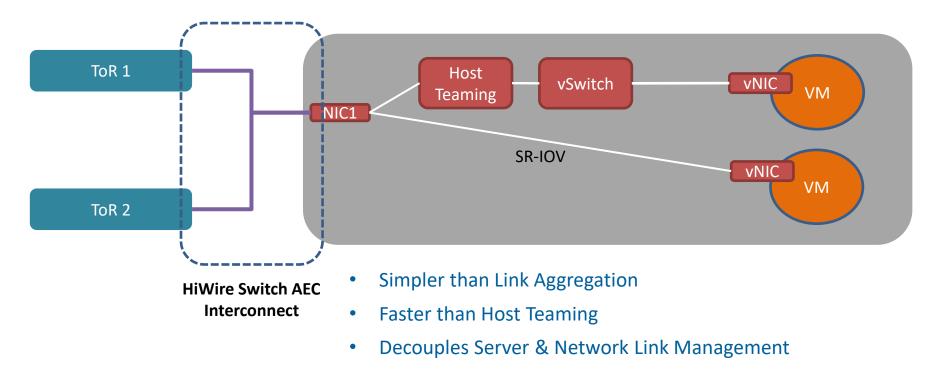
- Gearbox, FEC translation
- Networking device, not a DAC
- Added at integration to preserve flexibility right up to deployment
- line rate encryption





The Solution Space – Simplicity & Performance

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How do we deploy?

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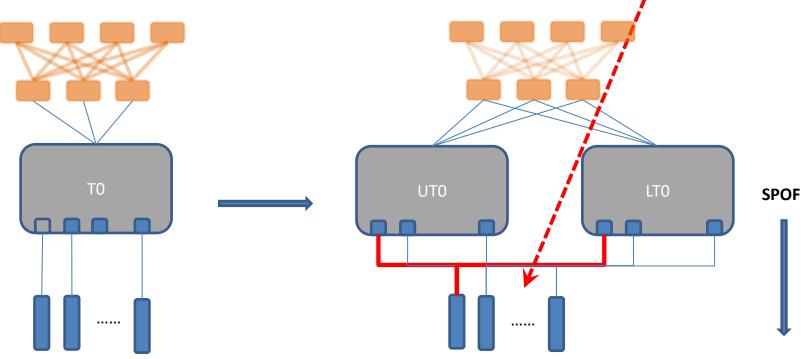






Dual ToR with Switch AEC

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Single TOR : Failure Domain: Rack (20 – 40 Servers)

Dual TOR + Switch AEC : Failure Domain: Single Server

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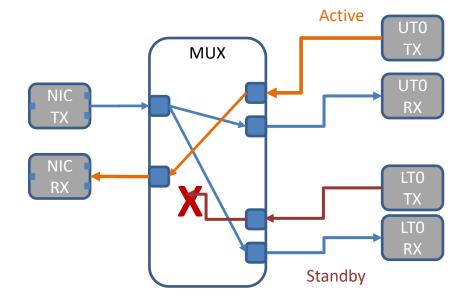
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Switch AEC Data Plane Behavior

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NIC TX is always broadcasted

NIC RX will take active side and only Switch with Loss-ofsignal or on-command over time < 1ms









Independent Active/Standby Design

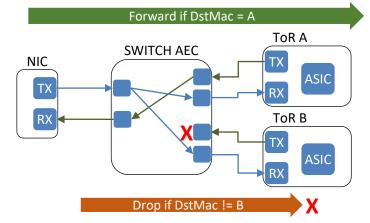
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Discovery via ICMP PING Ping with SrcMac = A SWITCH AEC TX RX TOR A TOR A SWITCH AEC TX ASIC RX Ping with SrcMac = B

Southbound Traffic is MUX'd

- Both ToRs both ping NIC,
- ToR A ping is forwarded to NIC
- ToR B ping is dropped by SWITCH AEC
- NIC learns ToR A as destination MAC

Active/Standby detection



Northbound traffic broadcasted to both ToRs

- ToR A: Forward north based on DstMac correctness
 - Verifies link integrity
- ToR B: Drop packets due to wrong DstMac
 - Sniffs to verify ToR A's link integrity

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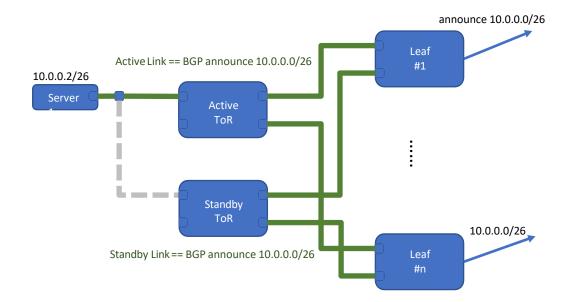
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HiWire™ Active Electrical Cable

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Routing Behavior – Both ToRs announce via BGP

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Both ToRs see Server (10.0.0.2/26) and announce to Leaf Nodes

All Leaf nodes see ToR announcements and announce to high level via standard BGP

Southbound traffic can arrive at either ToR

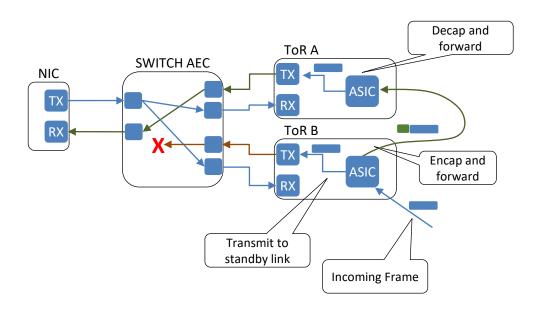
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Southbound Packet Path

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- Southbound traffic arriving on active ToR A is forwarded to NIC
- Southbound traffic arriving on Standby ToR B
 - ToR B encaps and transmits to ToR A
 - Tunneled through leaf instead of ISL
 - ToR A decaps and forwards to NIC
- Encap/decap handled by ASIC with minimum overhead

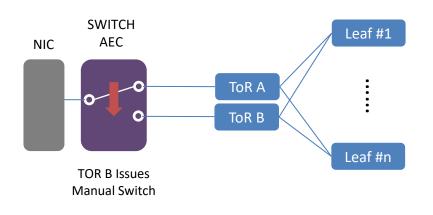






Failure Scenarios 1: Planned Maintenance

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Scenarios:

ToR OS upgrade, hardware replacement

- 1. ToR B proactively issues command to switch MUX to ToR B
- 2. ToR B ICMP Ping is now forwarded; ToR A is blocked
- 3. NIC updates DstMaC to ToR B
- 4. ToR B becomes active, ToR A become standby

Convergence time < 100ms

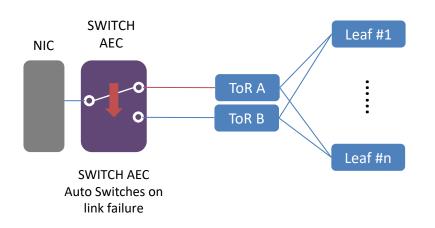






Failure Scenarios 2: Hardware Failure for ToR and Link

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Deployment Scenario:

Cable cut, ToR port failure, ToR power failure, ToR

- 1. Cable detect Loss-of-sigal to ToR A
- 2. Cable auto switches on link failure in less than $1\mu s$
- 3. ToR B ICMP is now forwarded; ToR A is blocked
- 4. NIC changes DstMac address
- 5. ToR assumes Active role

Convergence time < 100ms



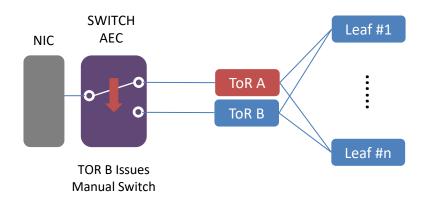






Failure Scenarios 3: ToR Forwarding Failure

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Deployment Scenario:

ToR grey failure impacts forwarding, but link remained up. Actually ~26 scenarios here – review SONiC Dual TOR Container docs

- 1. ToR B times out on sniffed ToR A Pings
- 2. ToR B Initiates Manual Switch of MUX
- 3. ToR B ICMP is now forwarded; ToR A is blocked
- 4. NIC changes DstMac address
- 5. ToR assumes Active role

Convergence in <100ms





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Demo Video

Terminal screen recording – end to end setup – no packet lost in middle

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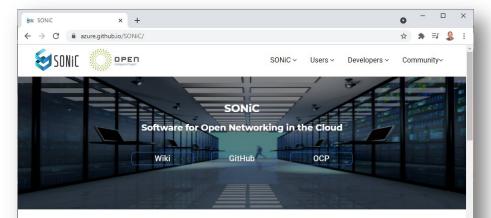




SONiC Support for HiWire SWITCH AEC

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- SWITCH AEC fully upstreamed and abstracted in SONiC
 - Support for any SONiC hardware
 - Hitless firmware updates from any cable end
 - Advanced Cable telemetry, loopback, BER and debug
- 50G and 100G HiWire[™] SWITCH AEC GA now!
- SONIC Dual TOR Management Container will be released in master SONIC branch after July 21
- HiWire SWITCH AEC + Dual TOR Management Container work out of box with SONiC



What is SONIC ?

SONIC is an open source network operating system based on Linux that runs on switches from multiple vendors and ASICs. SONIC offers a full-suite of network functionality, like BGP and RDMA, that has been production-hardened in the data centers of some of the largest cloud-service providers. It offers teams the flexibility to create the network solutions they need while leveraging the collective strength of a large ecosystem and community.

Decouples Hardware & Software

SONIC is built on Switch Abstraction Interface that helps in accelerating hardware innovation

Accelerates Software Evolution

irst solution to break monolithic switch software into multiple containerized components that accelerates software

Rapidly Growing Ecosystem

SONiC has gained wide industry support over the last year that includes major network chip vendors

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Time for Q&A

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Thank You

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