



200G ZEROFLAP OPTICAL TRANSCEIVER

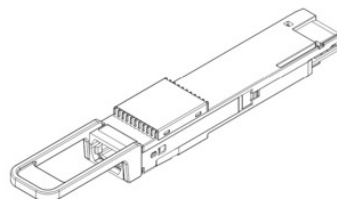
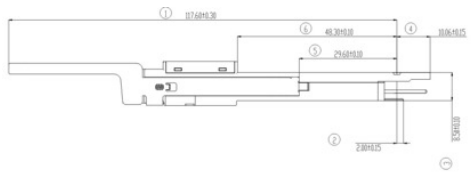
200GBASE-DR2 ZeroFlap Transceiver

200Gbps QSFP56, DR2, 500m

The **Credo ZeroFlap 200GBASE-DR2 Optical Transceiver** is specifically designed for use in AI fabrics and data center networks where enhanced reliability and visibility are essential. When used in conjunction with Credo PILOT software and integrated with the ZeroFlap Host SDK for telemetry harvesting and coordination, the ZeroFlap 200GBASE-DR2 Optical Transceiver forms an integral part of the Credo's complete ZeroFlap networking solution.

Featuring optical performance compliant with IEEE 802.3-2022 Clause 124.7, the ZeroFlap 200GBASE-DR2 Optical Transceiver gives network operators flexibility to deploy optical connections up to 500m using parallel single mode fiber with MPO-12 optical connections. Enhanced link telemetry reporting and autonomous link health monitoring provide complete network visibility and offers advanced warning for impending problems. The non-volatile event log embedded in the transceiver is a critical resource for accurately tracing historical network events, enabling efficient cluster maintenance.

The QSFP56 form factor is designed for use with 200G host ports supporting four (4) electrical lanes of 50Gbps following the IEEE 200GAUI-4 C2M standard. The optical interface employs two (2) optical lanes of 100Gbps using 1310nm wavelengths.



Features

- 200Gbps over parallel SMF
- Up to 500m transmission
- 1310nm Wavelength
- MPO12 – APC Fiber Connection
- ZeroFlap Link Telemetry
- Optical Link Health Monitoring
- FEC monitor, MPI detection
- Non-volatile event logging
- Remote peer communication
- Hitless firmware update
- Hot Pluggable
- Operating Temp 0-70oC
- RoHS-6 Compliant

Supported Standards

- Common Management Interface Specification (CMIS) v5.1
- QSFP56 MSA
- IEEE 802.3-2022 Clause 124.7
- IEEE 200GAUI-4
- OIF CEI-56G-VSR-PAM4

Product Details

Parameter	Value	Comments
Module Form Factor	QSFP56	QSFP56 MSA
Number of Optical Lanes	2 TX and 2 RX	200GBASE-DR2
Number of Electrical Lanes	4 TX and 4 RX	200GAUI-4, CEI-56G-VSR-PAM4
Maximum Aggregate Data Rate	212.5 Gb/s	
Electrical Interface and Pin-out	38-pin edge connector	QSFP56 MSA
Optical Interface	MPO12/APC	
Fiber Type	Parallel Single Mode Fiber	
Maximum Power Consumption	7W	
Management Interface	Serial I ² C	CMIS v5.1, 400 kHz maximum frequency

Ordering Information

Part Number	Form Factor	Data Rate	Distance (m)	Wavelength (nm)	Voltage (V)	Temperature (°C)
CFZ2D2S1M12Q-A1-HW	QSFP56	200Gbps	500	1310	3.3	0-70

About Credo

Credo's mission is to advance high-speed connectivity solutions that deliver optimized performance, reliability, energy efficiency, and security for the next generation of AI driven applications, cloud computing, and hyperscale networks.

Optimized for both optical and electrical applications, our solutions support port speeds up to 1.6Tb. At the core of our technology is our proprietary Serializer/Deserializer (SerDes) IP. Our diverse solutions portfolio includes system-level products such as Active Electrical Cables (AECs), a range of Integrated Circuits, including Retimers, Optical DSPs, SerDes chipsets, and SerDes IP Licensing.

For more information please visit www.credosemi.com or email sales@credosemi.com

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